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**In the Specification**

Please insert the following paragraph on page 6, following line 29:

C<sup>3</sup>      Optionally, the memory device can include protect circuit 149 to provide protection features to command execution logic 130. Optional low Vcc circuit 125 and latch circuit 127 can be included to detect a low Vcc voltage and latch data in response to the detected low Vcc.

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